

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	26	"7"?? clas. cache near5 level\$1 near10 (advantage and (speed or performance))	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/01/05 19:15
L4	26	L3	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/01/05 19:15
L5	9	"712" clas. ("read only memory" or ROM) with (microcode or microinstruction\$1) with lookup	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/01/05 19:15
L6	9	L5	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/01/05 19:15
S1	5	"instruction pointer queue"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 08:38
S2	92	"712" clas. (microinstruction\$1 or micro-instruction\$1 or UIP\$1 or UOP\$1) with (predict\$4 or speculat\$4)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 08:42
S3	37	"712" clas. (microinstruction\$1 or micro-instruction\$1 or UIP\$1 or UOP\$1 or micro-operation) with (predict\$4 or speculat\$4) (microinstruction\$1 or micro-operation\$1 or micro-instruction\$1) near5 pointer\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 08:45
S4	39	"712" clas. (microinstruction\$1 or micro-instruction\$1 or UIP\$1 or UOP\$1 or micro-operation) with (predict\$4 or speculat\$4) (microinstruction\$1 or micro-operation\$1 or micro-instruction\$1) near8 pointer\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 08:44
S5	22	"712" clas. (microinstruction\$1 or micro-instruction\$1 or UIP\$1 or UOP\$1 or micro-operation) with (predict\$4 or speculat\$4) (microinstruction\$1 or micro-operation\$1 or micro-instruction\$1) near5 pointer\$1 intel.as.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 08:48

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S6	15	"712".clas. (microinstruction\$1 or micro-instruction\$1 or UIP\$1 or UOP\$1 or micro-operation) with (predict\$4 or speculat\$4) (microinstruction\$1 or micro-operation\$1 or micro-instruction\$1) near5 pointer\$1 not intel.as.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 08:49
S7	97	(queue\$1 or buffer\$1 or register\$1) near5 pointer\$1 with (microinstruction\$1 or micro-operation\$1)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 09:40
S8	204	(microinstruction\$1 or micro-operation\$1 or micro-op\$1 or uop\$1) near5 pointer\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 09:41
S9	134	"712".clas. (microinstruction\$1 or micro-operation\$1 or micro-op\$1 or uop\$1) near5 pointer\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 09:44
S10	47	"712".clas. uop near2 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 09:44
S11	2619	pointer near2 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 09:56
S12	204	"712".clas. pointer near2 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 09:56
S13	43	"712".clas. pointer near2 queue (micro-op\$1 or micro-operation\$1 or microinstruction\$1 or uop\$1)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/17 09:56
S14	145	712/211.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 11:30
S15	0	712/211/245/246.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 08:42
S16	720	(712/211 or 712/245 or 712/246). ccls.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 11:47

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S17	2658	(712/211 or 712/245 or 712/246). ccls. (instruction near2 pointer\$1) or UIP	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 08:43
S18	84	(712/211 or 712/245 or 712/246). ccls. ((instruction near2 pointer\$1) or UIP)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 09:14
S19	7	(712/211 or 712/245 or 712/246). ccls. ((instruction near2 pointer\$1) or UIP) with queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 09:03
S20	14	(712/211 or 712/245 or 712/246). ccls. ((instruction near2 pointer\$1) or UIP) with buffer	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 09:19
S21	5	(712/211 or 712/245 or 712/246). ccls. ((instruction near2 pointers) or UIP) with buffer	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 09:38
S22	140	TPIPE	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 09:43
S23	1	"712".clas. TPIPE	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 09:43
S24	6	712/211.ccls. (long short) near5 instruction\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 09:53
S25	7	712/211.ccls. (long short) near15 instruction\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 09:53
S26	14	712/211.ccls. microsequenc\$4	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 11:31
S27	41	"instruction address" near2 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 15:43
S28	1	(712/211 or 712/245 or 712/246). ccls. "instruction address" near2 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 11:47

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S29	330	instruction near5 address near1 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/22 11:50
S30	207	"712".clas. instruction near5 address near1 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/01/05 19:15
S31	69	intel.as. (multitude or several or two or plurality or many) near3 (PLA\$1 or "programmable logic arrays" FPGA\$1 or "field programmable gate arrays")	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/23 09:45
S32	7	(712/211 or 712/245 or 712/246). ccls. intel.as. (multitude or several or two or plurality or many) near3 (PLA\$1 or "programmable logic arrays" FPGA\$1 or "field programmable gate arrays")	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 13:32
S33	27	(712/211 or 712/245 or 712/246). ccls. (multitude or several or two or plurality or many) near3 (PLA\$1 or "programmable logic arrays" FPGA\$1 or "field programmable gate arrays")	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/23 09:47
S34	10	(712/211 or 712/245 or 712/246). ccls. intel.as. (PLA\$1 or "programmable logic arrays" FPGA\$1 or "field programmable gate arrays")	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/23 09:51
S35	10	"patch cache"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 13:33
S36	2	"712".clas. "patch cache"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 13:33
S38	0	"712.clas" "L2 cache" with (speed or performance or advantage)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:33
S44	0	"712.clas" cache near5 level\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:33
S45	0	"712.clas" cache near5 level\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:34

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S46	10127	cache near5 level\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:34
S47	8339	"7"?? clas. cache near5 level\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:34
S48	1178	"7"?? clas. cache near5 level\$1 with (advantage or speed or performance)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:34
S49	950	"7"?? clas. cache near5 level\$1 near10 (advantage or speed or performance)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:35
S50	23	"7"?? clas. cache near5 level\$1 near10 (advantage and (speed or performance))	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:48
S51	1826	"7"?? clas. ("read only memory" or ROM) with (microcode or microinstruction\$1)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:50
S52	0	"7"?? clas. ("read only memory" or ROM) with (microcode or microinstruction\$1) with loopu	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:50
S53	0	"7"?? clas. ("read only memory" or ROM) with (microcode or microinstruction\$1) with loopup	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:50
S54	11	"7"?? clas. ("read only memory" or ROM) with (microcode or microinstruction\$1) with lookup	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:51
S55	9	"712" clas. ("read only memory" or ROM) with (microcode or microinstruction\$1) with lookup	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/03/24 16:51